

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	28	(differential adj amplifier) with semiconductor with four	USPAT	OR	ON	2006/10/31 14:32
L3	81	(differential adj amplifier) near4 transistors near4 four	USPAT	OR	ON	2006/10/31 14:33
L4	0	(differential adj amplifier) near4 transistors near4 four with (benefit advantage)	USPAT	OR	ON	2006/10/31 14:48
L5	0	(differential adj amplifier) near4 transistors near4 four same (benefit advantage)	USPAT	OR	ON	2006/10/31 14:33
L6	0	(differential adj amplifier) near4 transistors near4 four with (benefit advantage best better optimal optimum)	USPAT	OR	ON	2006/10/31 14:49
L7	0	(differential adj amplifier) near4 transistors near4 four with (benefit advantage best better optimal optimum maximum)	USPAT	OR	ON	2006/10/31 14:49
L8	0	(differential adj amplifier) with transistors with four with (benefit advantage best better optimal optimum maximum)	USPAT	OR	ON	2006/10/31 14:49
L9	2	(differential adj amplifier) same transistors with four with (benefit advantage best better optimal optimum maximum)	USPAT	OR	ON	2006/10/31 14:50
L10	8	(differential adj amplifier) same transistors with three with (benefit advantage best better optimal optimum maximum)	USPAT	OR	ON	2006/10/31 14:55
L11	101	high near2 gain near2 (operational adj amplifier) same (benefit advantage best better optimal optimum maximum)	USPAT	OR	ON	2006/10/31 14:57
L12	19	high near2 gain near2 (operational adj amplifier) with (benefit advantage best better optimal optimum maximum)	USPAT	OR	ON	2006/10/31 14:57
S1	9	(double adj pole) with (single adj throw) with switch with antenna	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 11:45

EAST Search History

S2	0	(biphase bi-phase (bi adj phase)) with IQ with receiver with modulation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 14:05
S3	0	(biphase bi-phase (bi adj phase)) with (I adj Q) with receiver with modulation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 14:06
S4	6	(biphase bi-phase (bi adj phase)) same (I adj Q) same receiver same modulation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/17 14:06
S5	271	327/408.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:33
S6	13	gresham-robert-ian.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:47
S7	0	("2005/0007196").URPN.	USPAT	OR	OFF	2006/05/10 17:44
S8	6	("20020125924" "20030001677" "6073002" "6157257" "6393260" "6518842").PN. OR ("6987419").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/10 17:44
S9	0	jp58-181310	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:48
S10	1	jp adj 58-181310	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:51

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S12	238	(isolation isolated isolate) same (transmit transmission transmitting transmitted) same (control bias) same transistor same channel and switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 11:53
S13	5	S12 and (gilbert dpst (double adj pole adj single adj throw))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:57
S14	4161	switch same (microwave millimeter) with (wave frequency frequencies)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:58
S15	1447	S14 and ((second two) with input)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:58
S16	167	S15 and ((dummy match) with (impedance))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 17:59
S17	47	S16 and switch.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 18:04
S18	6	(differential adj amplifier) same (isolation isolated isolate) same (transmit transmission transmitting transmitted) same (control bias) same transistor same channel and switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/31 14:23
S19	3186	(differential with (multiplexer multiplexing))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 18:09

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S20	1213	S19 and (differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 18:09
S21	595	S20 and transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/10 18:09
S23	552	S21 and input and output and (control bias)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 09:20
S24	1589	330/51.ccis.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 09:21
S25	14	("20020125924" "20030001677" "6073002" "6157257" "6393260" "6518842" "6987419").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 09:47
S26	273	S24 and (Differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 10:25
S27	88707	(Differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 10:25
S28	1630	S27 and switch.ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 10:26

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S29	380	S28 and (first second) near3 input	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 10:26
S30	5	("4916410" "5159296" "5170139" "5440283" "5883541").PN. OR ("6819201").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/11 10:34
S31	3188	((double adj pole) with (single adj throw)) (DPSTS DPST)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 11:46
S32	101	S27 and S31	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 11:53
S33	5291	((isolation isolated isolate) and (transmit transmission transmitting transmitted)) with channel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 11:53
S34	234	S27 and S33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 11:54
S35	190	S34 and switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 12:02
S36	15810	(microwave millimeter) same switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 12:02
S37	344	S36 and S27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 12:16

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S39	2747	(two second) with (input) with (AC (alternating adj current)) with switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 12:17
S40	137	S39 and S27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 12:17
S41	998	330/295.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 13:41
S42	153	S41 and S27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 14:45
S43	408	(solid with state with components) with (integrated circuit) same (benefit advantage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 14:46
S44	20	S43 and S27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/11 14:46
S45	2177	307/112.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/11 15:30
S46	1589	330/51.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/11 15:31

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S47	662	327/407.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/11 15:32
S48	271	327/408.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/11 15:49
S49	2	"2002125924"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/11 15:50
S50	2	"2002125924".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/11 15:53
S51	2	("6466064").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/11 15:56
S52	2	("6784743").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/11 15:56